Improved Non-Isolated Bidirectional DC-DC Converter Assisted with an Active Resonant Network

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Abstract— This paper proposes a new non-isolated bidirectional dc-dc converter (NI-BDC) with an auxiliary resonant network (ARN) that is typically operated in both boost and buck modes. Regardless the direction of power flow, softswitching capabilities such as zero voltage switching (ZVS) and zero current switching (ZCS) conditions has been obtained under light and heavy loads. This converter avoids the need of coupled inductors as against conventional converters thus reducing the size of the converter and minimizes the device count. The auxiliary resonant network mainly consisting of resonant inductor, capacitor, auxiliary switch and a diode. Furthermore, the zero voltage switching and zero current switching conditions have been achieved in both boost and buck modes that reduces reverse recovery conduction period of the body diodes of all the power switches. The proposed converter operates with soft-switching at constant switching frequency giving continuous inductor current. The detailed operating principles for boost and buck modes are described. The theoretical analysis and simulation results are presented.

Keywords— Active resonant network, Bidirectional converter, soft-switching, zero voltage switching (ZVS), zero current switching (ZCS)

I. INTRODUCTION

Non-Isolated Bidirectional dc-dc converters (NI-BDC) are commonly used in many energy storage applications like battery storages systems (BSS) [1], electric vehicle charging stations (EV-CS) [2] and other renewable energy systems such as fuel cell and solar cell [3]. The amplitude of reverse currents [4] through the main switching devices are minimized mainly strategies through control operated under discontinuous conduction mode. The zero voltage switching transitions are obtained at different loads. Regardless of power transfer, minimized switching stresses, reduced current ripples and improved battery life, are the significant aspects to develop an efficient bidirectional converter. To achieve these advantages, a coupled inductor, auxiliary switches and auxiliary inductor are used in a bidirectional converter (BDC) [5]. In addition to the aforementioned merits other literatures focused on reducing the usage of auxiliary switches and eliminating the control complexity. BDCs implemented with an auxiliary winding [6-7] in addition to main inductor and an auxiliarv inductor to provide the ZVS turn-on feature. However, this converter reduces switching stresses at light load conditions. In contrast to the previous soft-switching methods, combination of both coupled inductors and auxiliary switches [8] are sufficient to obtain zero voltage switching condition without using an additional inductor. Besides, the turn-on losses are greatly reduced at light load conditions. However, this converter suffers from high turn-off losses at low output power.

Different approaches were proposed in developing softswitching based non-isolated bidirectional converter are classified as below:

- 1. In the first approach, in order to eliminate the large circulating current and minimal reverse recovery phenomenon, an active snubber circuit is used to reduce turn-on losses that includes lossless diodes, auxiliary switches, inductor, and capacitor in addition to the main switching devices. The ZVS condition is obtained in both the converters [9-10]. Besides, simple auxiliary cell [10] based converter can have more circulating current and conduction losses at light loads. However, active snubber based BDC drastically increases the total number of auxiliary device count. The operating switching periods of main MOSFETs and auxiliary MOSFETs are almost same. Further, the ZVS condition causes reduced voltage and current stresses. The conduction losses are more in auxiliary MOSFETs due to their wide operating turn-on periods.
- 2. Another approach is to use lossless passive snubber cell adopted in a BDC [12] without using auxiliary switches to achieve ZVS turn-on in both the boost and buck modes. It aims to minimize the circulating currents at light loads. However, the total numbers of passive devices are increased and coupled inductor may lead to increase the additional stresses.

This paper proposes a new soft-switching bidirectional converter with soft turn-on and soft turn off through a simple auxiliary circuit without increasing the additional losses. The next section presents description and operation principles of the converter and section III presents the simulation results.

II. DESCRIPTION OF PROPOSED BIDIRECTIONAL CONVERTER AND ITS PRINCIPLES OF OPERATION

Fig.1 shows the ZVS bidirectional converter topology [5] with an additional coupled inductor, auxiliary diodes D_{a1} , D_{a2} , auxiliary MOSFETs, S_{a1} , S_{a2} , and auxiliary capacitors, C_{a1} , C_{a2} . The proposed soft-switching bidirectional converter shown in Fig.2 is the modified version of the topology shown in Fig.1 with reduced auxiliary devices. It consists of an input inductor, L, two MOSFETs, S_1 , S_2 , a capacitor, C_o , and an active resonant network (ARN) which consists of an auxiliary MOSFET, S_a , a resonant inductor, L_a , a resonant capacitor, C_a and a diode, D_a . When the proposed converter is operating in

boost mode, ARN provides the soft-switching condition to the main MOSFETs S_1 , S_2 . In buck mode operation, the same ARN provides soft-switching condition to the main switching devices. The operating principles of boost mode are divided into ten intervals from t₀-t₁₀, as shown in Fig.3 and Fig.4 shows the equivalent circuits of each interval. In similar way, operating intervals for buck mode are divided in to nine intervals t₀-t₉, as shown in Fig.5.



Fig. 1. ZVS Bidirectional DC-DC Converter [5]



Fig. 2. Proposed Bidirectional DC-DC Converter with ARN

A. Boost mode Principles of Operation

At t₀, S_I is turned-off and voltage across input inductor L changes its direction from $+V_I$ to $-V_I$. The current through L starts decreasing and continues throughout the interval. At t₁, S_2 is turned-on and output power transfers via L-body diode of S_I - R_o . At t₂, the auxiliary MOSFET, S_a is turned on, S_2 current falls to zero and body diode of S_2 starts conducting. Hence, the zero current switching (ZCS) turn off is achieved. During the interval, t₂-t₃, the resonant capacitor, C_a charges from $-V_I$ to $+V_I$. In the interval, t₃-t₄, S_I is turned off and S_a is already conducting. The voltage across S_I becomes zero and its body diode starts conducting. Thus, zero voltage switching (ZVS) condition is achieved. At t₄, S_I is turned-on. During the interval, t₄-t₅, the body diode of S_a is in conduction. The resonant capacitor C_a discharges to zero from $+V_I$. At t₅, S_a is

turned off and C_a starts charging from 0 to $-V_l$. At t₆, the current flows linearly.



Fig.3 Key waveform : Boost mode

B. Buck mode Principles of Operation

The operating principle of buck mode has been divided into nine intervals, t₀-t₉, as shown in Fig.3. At t₀, S_I is turned off and S_a is turned on. The current through L_a and voltage across C_a are at constant input current and voltage levels, respectively. Prior to t₁, voltage across S_2 is zero. At t₁, both i_{La} and V_{Ca} starts decreasing and body diode of S_2 starts conducting. Hence the zero voltage switching (ZVS) condition is achieved for S_2 . At t₂, S_2 is turned on and S_a is turned off. Throughout the interval t₂-t₃, i_{La} and V_{Ca} are constant. At t₃, S_2 is turned off, i_{La} starts increasing and C_a starts discharging. At t₄, S_I is turned on. At t₅, the body diode of S_I starts conducting and at t₆, it stops conducting. The time intervals t₆-t₉ are same as the intervals t₀-t₄.

C. Soft-switching Condition and Design Example

The soft-switching condition can be obtained for the main switches only when the parameter, y is less than one.

$$y = \frac{I_{\text{max}}}{V_1} \sqrt{\frac{L_a}{C_a}} \tag{1}$$

$$y_1 = \frac{L_a}{L} < 1 \tag{2}$$

Where; $I_{\text{max}} = \frac{P_{\text{max}}}{V_o}$; maximum input current, V_o = output

voltage.



 $\begin{array}{l} \mbox{Fig. 4. Equivalent circuitss of Boost mode : (a) Interval (t_0-t_1) (b) Interval (t_1-t_2) (c) Interval (t_2-t_3) (d) Interval (t_3-t_4) (e) Interval (t_4-t_5) \& (t_5-t_6) (f) Interval (t_6-t_7) \end{array}$

The soft-switching condition is obtained only if the following conditions are satisfied.

$$y_1 < 1$$
 (3)
 $y > y_1$ (4)

Design Example: Input voltage $V_1 = 100 V$; Output voltage $V_o=330 V$; Output power =1 kW; Switching frequency $f_s=50kHz$.

$$f_r = \frac{1}{2\pi\sqrt{L_a C_a}} \tag{5}$$

1. The maximum input current calculated for auxiliary resonant network is, $I_m=3A$

2. Resonant parameters L_a and C_a are obtained from equations (1) and (2), $L_a=20 \mu$ H; $C_a=40 n$ F

3. The characteristic impedance of the auxiliary resonant

network is given as $Z = \sqrt{\frac{L_a}{C_a}}$

Higher values of characteristic impedance results in lower current stress in the auxiliary resonant network.



Fig.5. Key waveform : Buck mode

III. SIMULATION RESULTS

The designed converter performance is validated through MATLAB simulations. The proposed converter simulations are performed for both boost and buck operating modes separately. The simulation parameters are considered as follows: Input voltage: 100V ; Output voltage: 330V; Switching frequency: 50kHz; Output power: 1kW; Input inductor: 100 μ H; Resonant inductor, L_a : 20 μ H; Resonant capacitor, C_a : 40nF : Output capacitor, C_o : 470 μ F.



Fig. 6. Simulated waveforms : Boost mode (a) V_{SI} (b) i_{SI} (c) V_{S2} (d) i_{S2}



Fig.7 Simulated waveforms: Boost mode (a) V_{Sa} (b) i_{Sa} (c) i_{La} (d) V_{Ca}



Fig.8 Simulated waveforms: Buck mode (a) V_{S1} (b) i_{S1} (c) V_{S2} (d) i_{S2}

Fig.6 shows the main switches, S_1 , S_2 , voltage and current waveforms. It is observed from the obtained results that ZCS turn off and ZVS turn on conditions are achieved. Fig.7 shows the auxiliary switch, S_a , L_a , C_a voltage and current waveforms. Similarly, the simulations are performed in buck mode and the obtained results are shown in Fig.8 & Fig.9. In the buck mode of operation, the soft-switching conditions are achieved without additional losses. Fig.10 shows the turn-on and turn-off transitons of MOSFETs S_1 , S_2 operated at 1.5 kW output power, clearly.



Fig.9. Simulated waveforms: Buck mode (a) V_{SI} (b) i_{SI} (c) i_{La} (d) V_{Ca}



Fig.10. Turn on and Turn off Transition of MOSFETs (a) S_1 (b) S_2

IV. CONCLUSION

This paper presents a novel non-isolated bidirectional converter with a simple auxiliary resonant network. The operating principles and simulation results are presented for a 100V/330V/1kW converter. The zero current switching (ZCS) turn off and zero voltage switching (ZVS) turn on conditions are achieved without considerably increasing losses for both boost and buck modes of operation. The proposed converter will be a better solution for future needs in energy storage applications with better efficiency.

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